| Substitute for form 1449A/PTO | | | | Complete if Known | | |
|---|-----|----|---|------------------------|------------------|--|
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | | | Application Number | Not yet assigned | |
| | | | | Filing Date | July 14, 2003 | |
| | | | | First Named Inventor | Chiang Pu | |
| | | | | Art Unit | Not yet assigned | |
| | | | | Examiner Name | Not yet assigned | |
| Sheet | 1 . | of | 1 | Attorney Docket Number | 42P16565 | |

| OTHER ART - NON PATENT LITERATURE DOCUMENTS | | | | | | | | | |
|---|--------------|---|--|--|--|--|--|--|--|
| Examiner Initials* | Cite No.' | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | | | | | | | |
| тм | | Su, David K and Wolley, Bruce A. "A CMOS Oversampling D.A converter with a Current-Mode Semidigital Reconstruction Filter," IEEE Journal of Solid-State Circuits, Vol. 28, No. 12, December 1993. | | | | | | | |
| TM | | Figueroa, Miguel, Hsu, David and Diorio, Chris, "A Mixed-Signal Approach to High-Performance Low-Power Linear Filters," IEEE Journal of Solid-State Circuits, Vol. 36, No. 5, May 2001. | | | | | | | |
| | • | | | | | | | | |
| | | | | | | | | | |
| | <u>.</u> | | | | | | | | |
| | - | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| İ | | | | | | | | | |

| Examiner /Tan Mai/ | Date Considered | 09/13/2006 |
|----------------------|--------------------|------------|
|----------------------|--------------------|------------|

^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

'Applicant's unique citation designation number. 'Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (05-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 05/02/2003. Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450